Do 254 For Fpga Designer White Paper By Xilinx

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Do 254 For Fpga Designer

While not considered a part of the hardware life cycle by DO-254, the hardware safety assessment does directly impact FPGA design. This assessment determines the DAL for each functional block in the system. A system developer has the option of setting a single design assurance level and strategy for an entire hardware item, or a hardware

DO-254 for the FPGA Designer - Xilinx

DO-254 Support for FPGA Design Flows Altera Corporation 4 transceiver block and package- and pin-compatibility to Stratix IV FPGAs that supports a seamless prototype-to-production path. An Altera DO-254 design flow can apply towards certification with a final system implemented either in FPGA or HardCopy ASIC. Secure Soft Processor Core

DO-254 Support for FPGA Design Flows - Intel

This white paper focuses on the details of developing a DO-254 compliant process for the design of FPGAs. The standard that governs the design of avionic components and systems, DO-254, is one of the most poorly understood but widely applicable standards in the avionic industry.

DO-254 for the FPGA Designer | Semantic Scholar

White Paper. DO-254 discusses the need for "Design Standards" and Order 8110-105 takes this a step further, discussing the specific need for HDL coding standards. Because of this, many companies having to comply with DO-254 are either looking for examples of good standards to use, or recognize that they have insufficient or inconsistent standards and want to improve their approach.

Understanding and Running DO-254 Coding Checks in HDL Designer

DO-254 ASIC/FPGA/Board Design ASIC/FPGA/Board Design, Development and Verification for DO-254 compliant systems based on size, intricacy and design assurance levels eInfochips can assist clients with DO-254 requirements and offer cost effective solution as per the changing forefront of the avionics industry.

DO-254 ASIC/FPGA/Board Design Services | Avionics Hardware ...

Hardware Design Processes (covered in RTCA/DO-254 Section 5) – The processes introduce a requirements-based design process, which means all of the design data must be based on the requirements. Any functions of the final FPGA that are not based on the requirements must be properly mitigated in order to prevent anomalous operational behavior.

Developing High-Reliability FPGAs For DO-254

FPGA verification for DO-254 is in the hardware Verifying a complex FPGA design under DO-254 guidelines for use in safety- and mission-critical airborne systems is not without its challenges. Louie De Luna, Aldec Europe’s Product Manager for DO-254, describes how an at-speed, in-hardware verification methodology can help.

FPGA verification for DO-254 is in the hardware

Developing PLDs (FPGAs, ASICs and CPLDs) for DO-254 compliance entails that applicants submit extensive professional documents and artifacts to the designated certification authority. It is the

applicant's responsibility to author and create the documents and review them with the highest scrutiny against a high-quality checklist.

DO-254 Checklist - The Design Verification Company
Design Assurance Guidance for Airborne Electronic Hardware. RTCA DO-254 / EUROCAE ED-80, Design Assurance Guidance for Airborne Electronic Hardware is a document providing guidance for the development of airborne electronic hardware, published by RTCA, Incorporated and EUROCAE. The DO-254/ED-80 standard was formally recognized by the FAA in 2005 via AC 20-152 as a means of compliance for the design assurance of electronic hardware in airborne systems.

DO-254 - Wikipedia
The standard that governs the design of avionic components and systems, DO-254, is one of the most poorly understood but widely applicable standards in the avionic industry. While information on the general aspects of the standard is easy to obtain, the details of exactly how to implement the standard are sketchy.

CiteSeerX — DO-254 for the FPGA Designer
Model-Based Design for DO-254 combines automation tools from MathWorks and Mentor Graphics for design and verification to support a development process that goes from concept through implementation. This paper discusses this flow.

Enabling Model-Based Design for DO-254 Compliance with ...
There exists little doubt that a fully compliant DO-254 process is required for custom FPGA designs and for the custom intellectual property (IP) that resides within them, but how can a designer use commercially available IP within a DO-254 compliant system? Here is where ambiguities enter the DO-254 process.

Xilinx WP403 Practical Use of FPGAs and IP in DO-254 ...
The DO-254 standard defines a set of objectives for hardware to be certified for use in airborne systems. It is modeled after DO-178, the equivalent standard for flight software certification. As with DO-178, satisfying DO-254 objectives can be expensive and time-consuming due to several processes: Requirements management and tracing

DO-254 - MATLAB and Simulink - MATLAB & Simulink
Because DO-254 is a high-level, process-oriented standard, responsibility for design assurance necessarily requires a team approach among designers and suppliers.

DO-254 Support for FPGA Design Flows | FPGA Central
HDL Detailed Design and Verification HDL development and verification under DO-254 guidelines is a rigorous undertaking and requires special features and capabilities from HDL design and simulation tools.

HDL Detailed Design and Verification
ECE's Engineering team can assist with DO-254 electronic design and development, DO-254 Review, DO-254 Firmware design, DO-254 hardware design, DO-254 FPGA design, and DO-254 Documentation. For more on DO-254, visit Wikipedia. Contact ECE at (419) 861-9000 x114 or ece_sales@eceinc.com to discuss how ECE can assist with your DO-254 project.

DO-254 Design and Review | Electronic Concepts ...
The DO-254 specification utilizes a requirements-based design and verification approach. This means that the entire hardware project revolves around a formal set of high-level requirements.

DO-254 Explained - Cadence Design Systems
20-152 - RTCA, Inc., Document RTCA/DO-254, Design Assurance Guidance for Airborne Electronic Hardware Date Issued July 05, 2005 Responsible Office